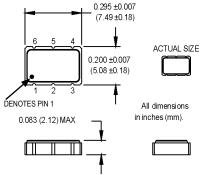
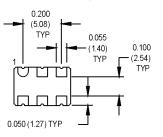
CMV & CMV3 Series 5x7 mm, 5.0 or 3.3 Volt, CMOS, VCXO

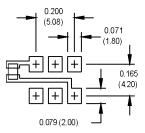








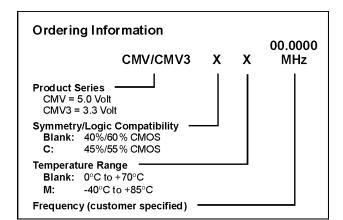
SUGGESTED SOLDER PAD LAYOUT



Pin Connections

PIN	FUNCTION				
1	Control Voltage				
2	Tristate				
3	Ground & Gnd Plane				
4	Output				
5	N/C				
6	+Vdd				





		1					
	PARAMETER	Symbol	Min.	Тур.	Max.	Units	Condition/Notes
-	Frequency Range	F	2		52	MHz	
	Operating Temperature	TA	(See Ordering Information)				
	Storage Temperature	Ts	-55		+125	°C	
	Frequency Stability	∆F/F					
	Overall		Inclusive of Calibration, Temperature, Voltage, Load, and Aging				
	0°C to +70°C		±25 ppm				
	-40°C to +85°C		±40 ppm				
	Aging						
	1st Year		-3		+3	ppm	
	Thereafter (per year)		-1		+1	ppm	
suo	Pullability/APR						
	2.000 to 45.000 MHz			±120		ppm	
	45.001 to 52.000 MHz			±90		ppm	
ati	Control Voltage	Vc	0.5	2.5	4.5	v	CMV
iţi	-		0.3	1.65	3.0	v	CMV3
spe	Linearity				5	%	Positive Monotonic Slope
Electrical Specifications	Modulation Bandwidth	fm	20			kHz	±3dB
iti	Input Impedance	Zin	50k			Ohms	@ 10 kHz
Ĭ	Input Voltage	Vdd	4.5	5.0	5.5	V	CMV
_			3.0	3.3	3.6	v	CMV3
	Input Current	ldd			30	mA	
	Output Type						HCMOS
	Load				15	pF	HCMOS
	Symmetry (Duty Cycle)		(See Ordering Information)			@ 50% Vdd	
	Logic "1" Level	Voh	Vdd -0.5			V	
	Logic "0" Level	Vol			0.5	V	
	Output Current				20	mA	
	Rise Time	Tr			5	ns	20% to 80% Vdd, CL=15pF
	Fall Time	Tf			5	ns	80% to 20% Vdd, CL=15pF
	Tri-state Function		Input Logic "1" or floating: output active				
			Input Logic "0": output disables to high-Z				
	Start up Time				10	ms	
	Phase Jitter @ 26 MHz	φJ		4		ps RMS	Integrated 12 kHz - 20 MHz
	Phase Noise (Typical)	10 Hz	100 Hz	1 kHz	10 kHz	100 kHz	Offset from carrier
	@ 26 MHz	-65	-95	-115	-130	-140	dBc/Hz

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